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PATENT
IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re Application of:)
)
Pekarek *et al.*)
)
Serial No. 10/611,314)
)
Filed: June 30, 2003)
)
For: Impedance Mismatch Modeling In A)
Data Flow Or Discrete Time Based)
System Simulation)
)
Examiner: Shambhavik K. Patel)
)
Group Art Unit: 2128)

RESPONSE AND AMENDMENT TO THE
FINAL REJECTION OF FEBRUARY 22, 2007

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Applicant submits the following Response and Amendment to the Final
Rejection of February 22, 2007.